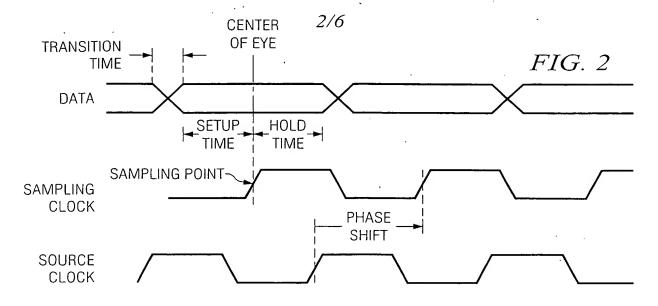
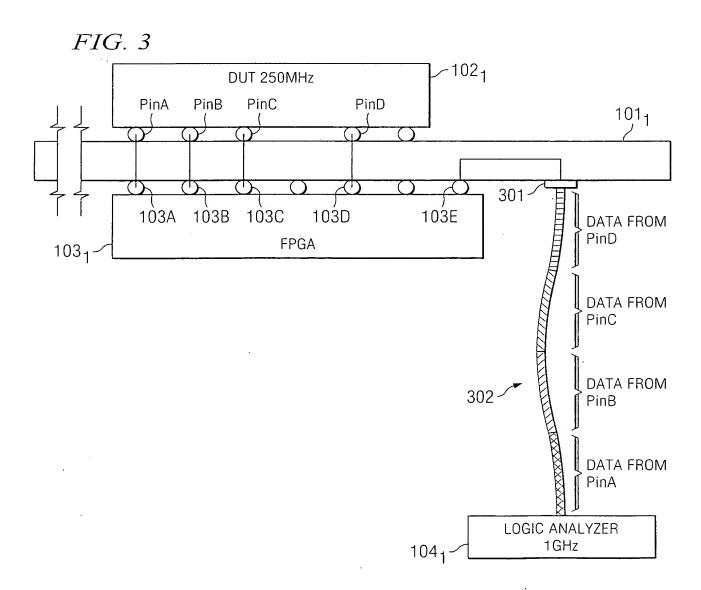
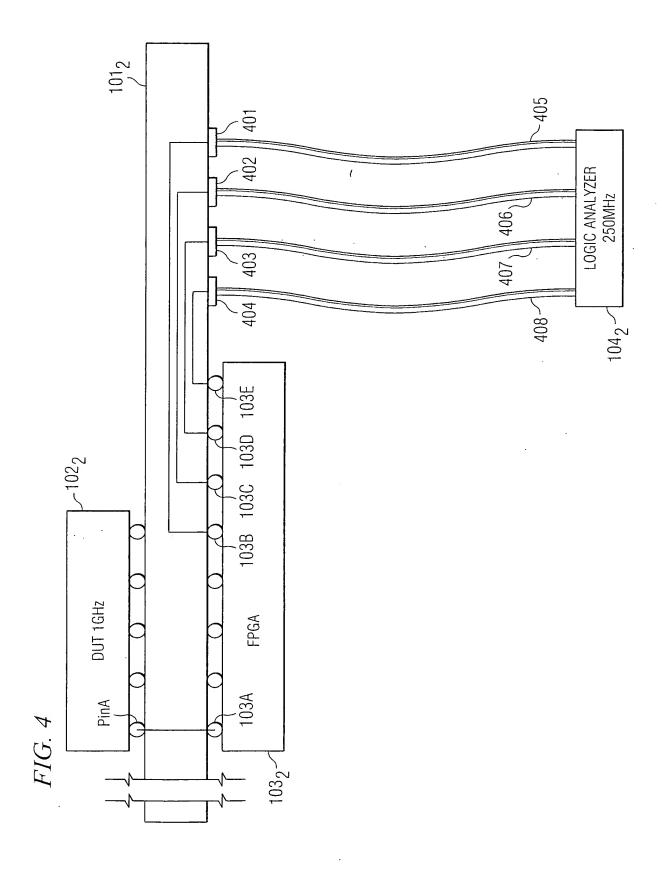
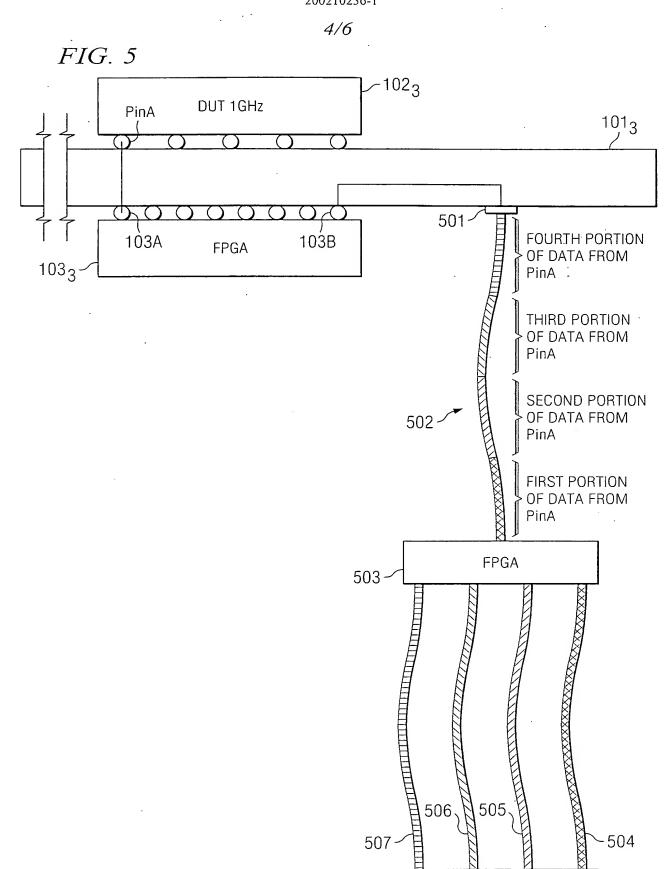


SYSTEM AND METHOD USING A PROGRAMMABLE DEVICE FOR CAPTURING SIGNALS FROM A DEVICE DURING TESTING Brian Johnson et al. 200210236-1









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LOGIC ANALYZER 250MHz

